



PATENT
0941-0316P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: KER, Ming-Dou et al. Conf.: 9842
Appl. No.: 09/944,171 Group: 2812
Filed: September 4, 2001 Examiner: UNASSIGNED
For: ESD PROTECTION CIRCUIT WITH VERY LOW
INPUT CAPACITANCE FOR HIGH-FREQUENCY I/O
PORTS

#3
D Scott
2-7-02

L E T T E R

Assistant Commissioner for Patents
Washington, DC 20231

January 28, 2002

Sir:

Enclosed herewith is a Supplemental Declaration that should be made part of the record of the present application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

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KM/asc
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Attachment

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